

Patent & Trademark Office

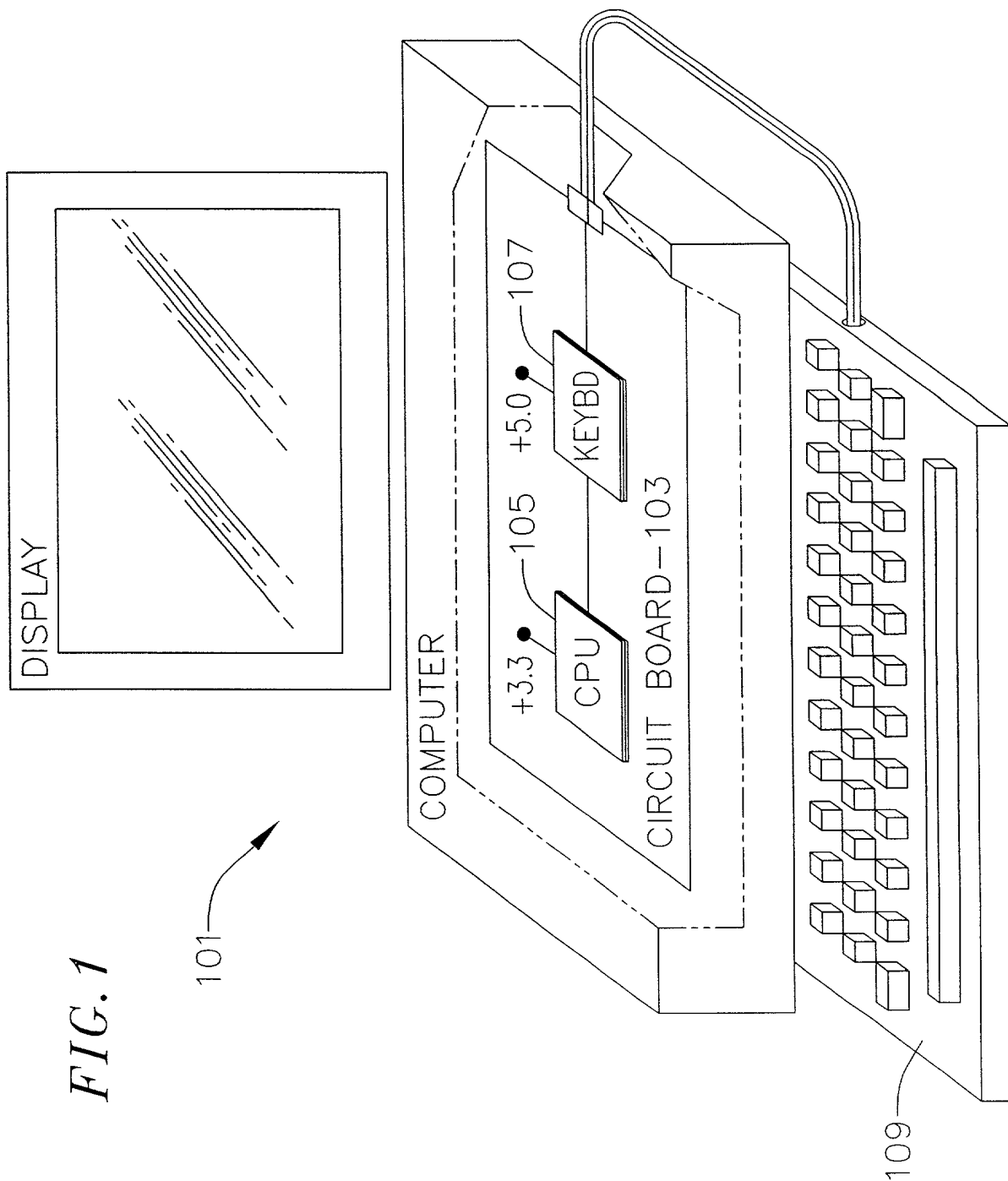


FIG. 1

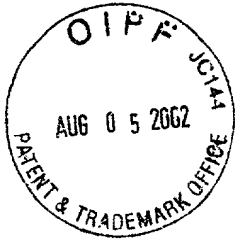


FIG. 2
PRIOR ART

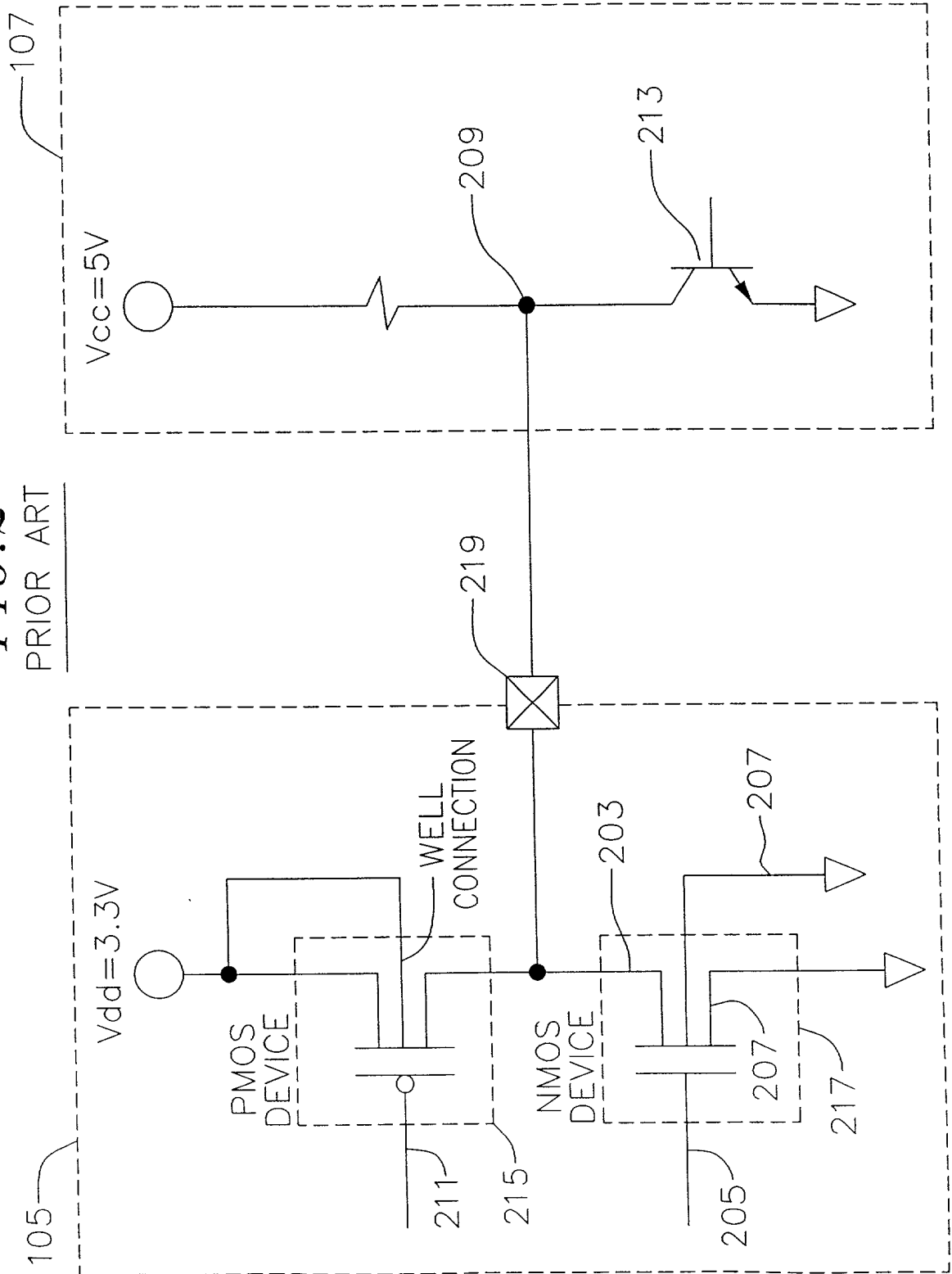
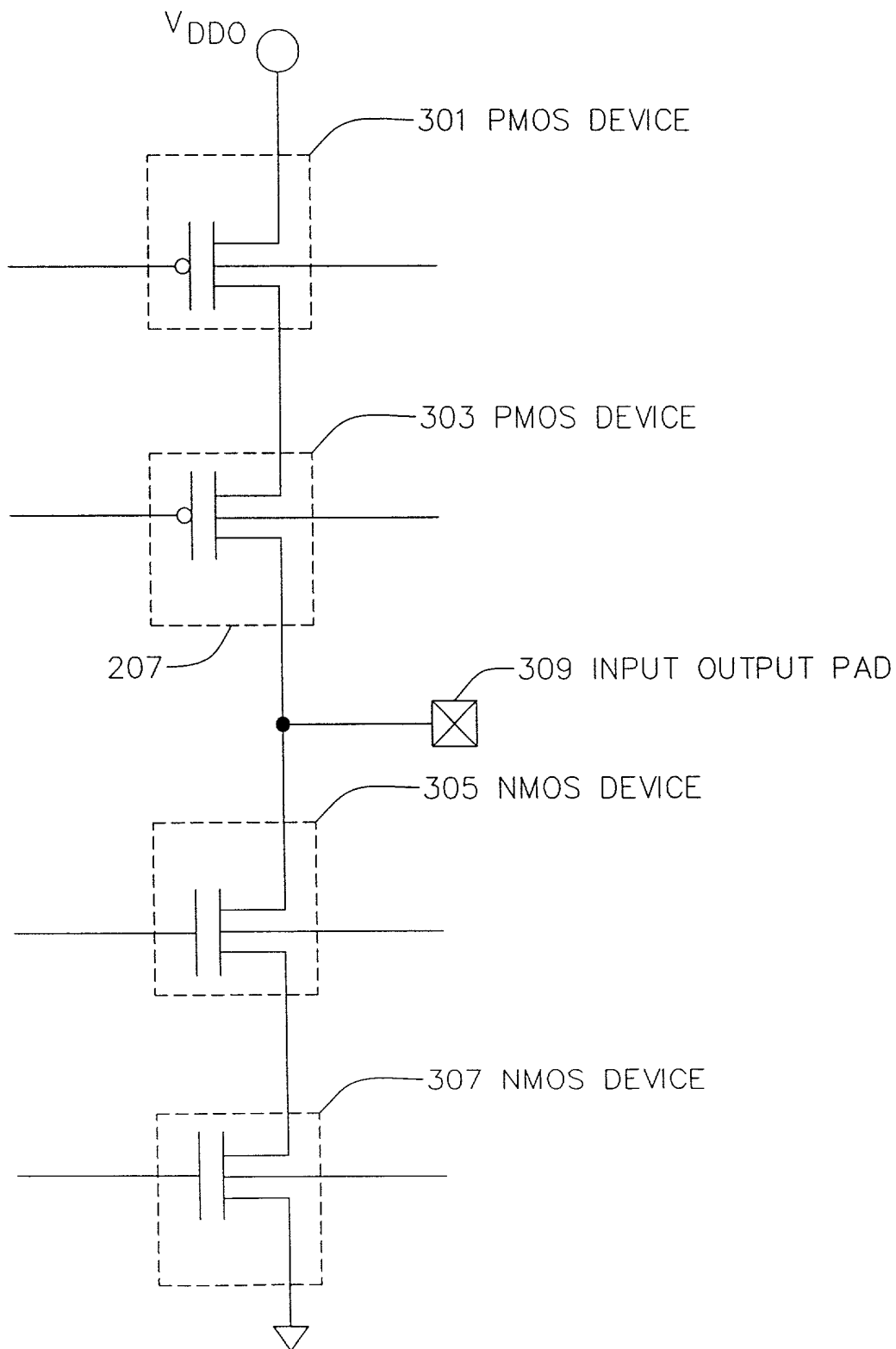




FIG. 3



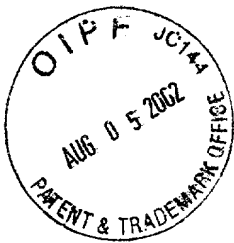
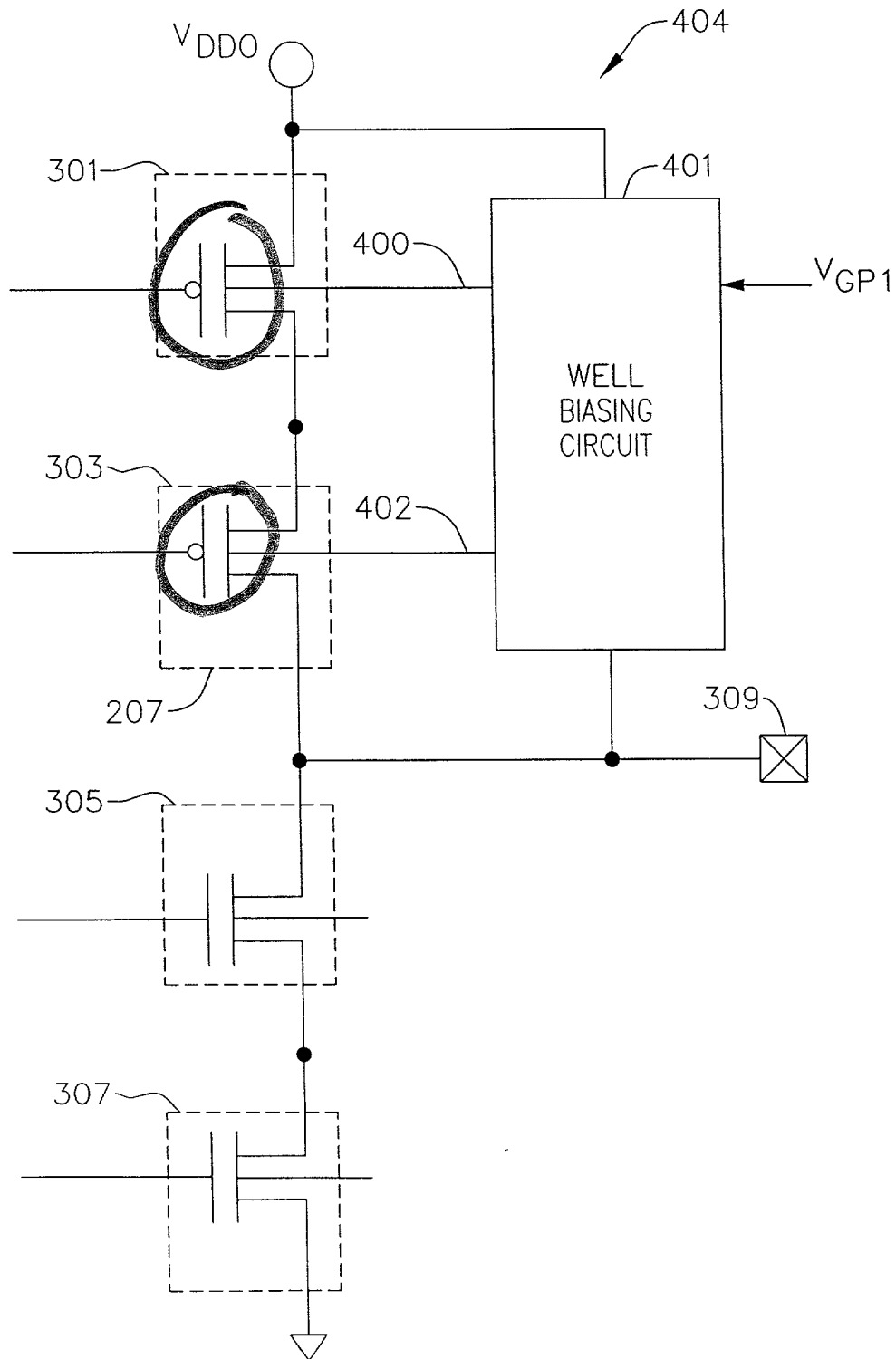
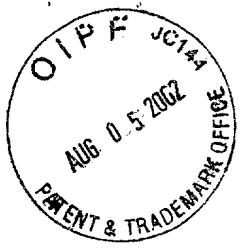


FIG. 4





Application No. 10/043,763
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
ACCOMMODATES LARGE POWER SUPPLY
VARIATIONS

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FIG. 5

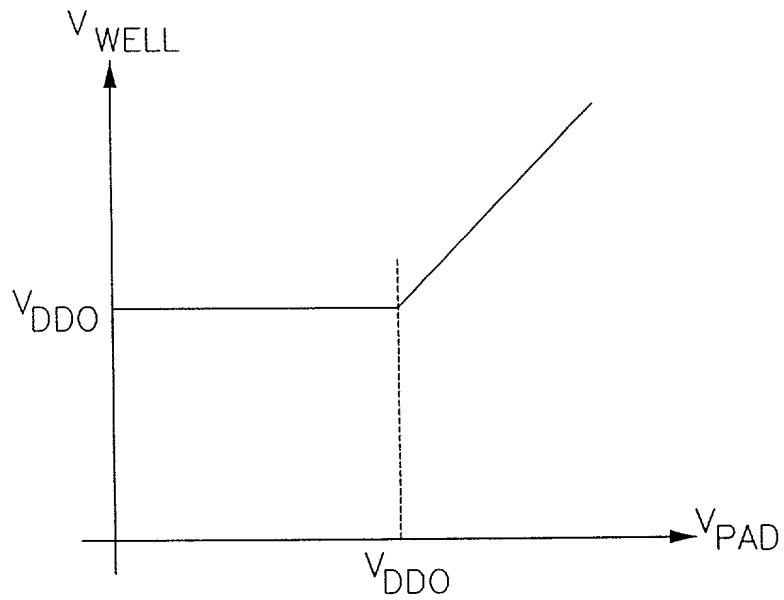
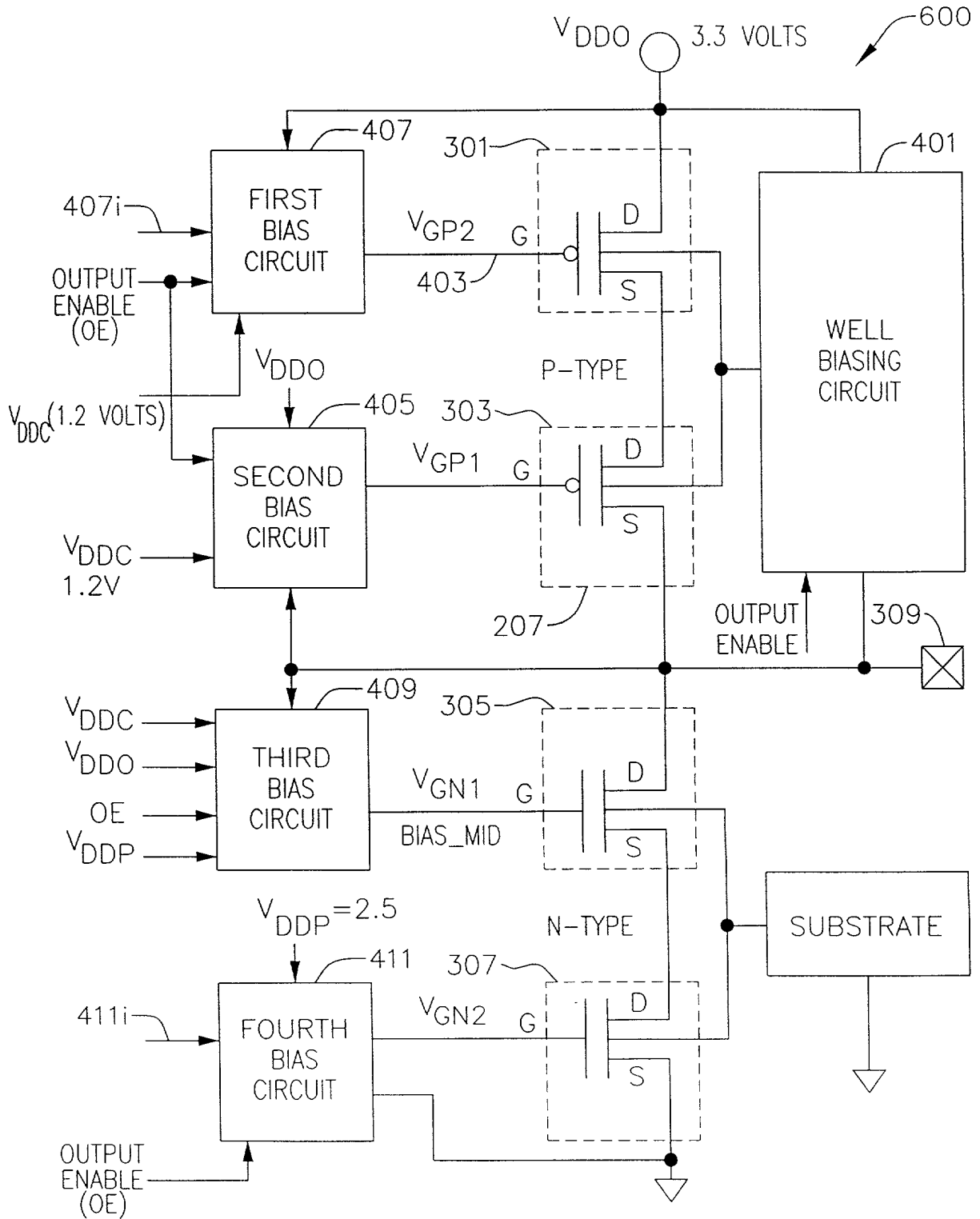


FIG. 6





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FIG. 7

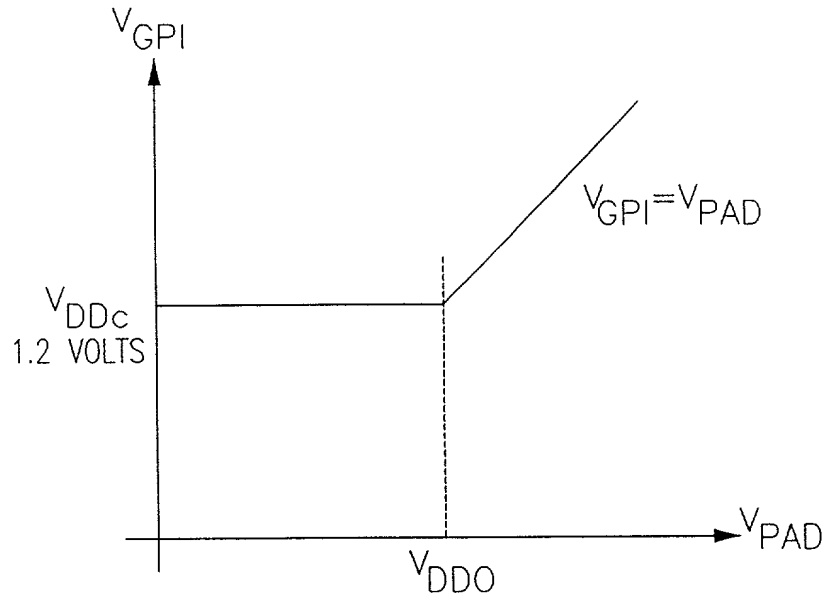




FIG. 8

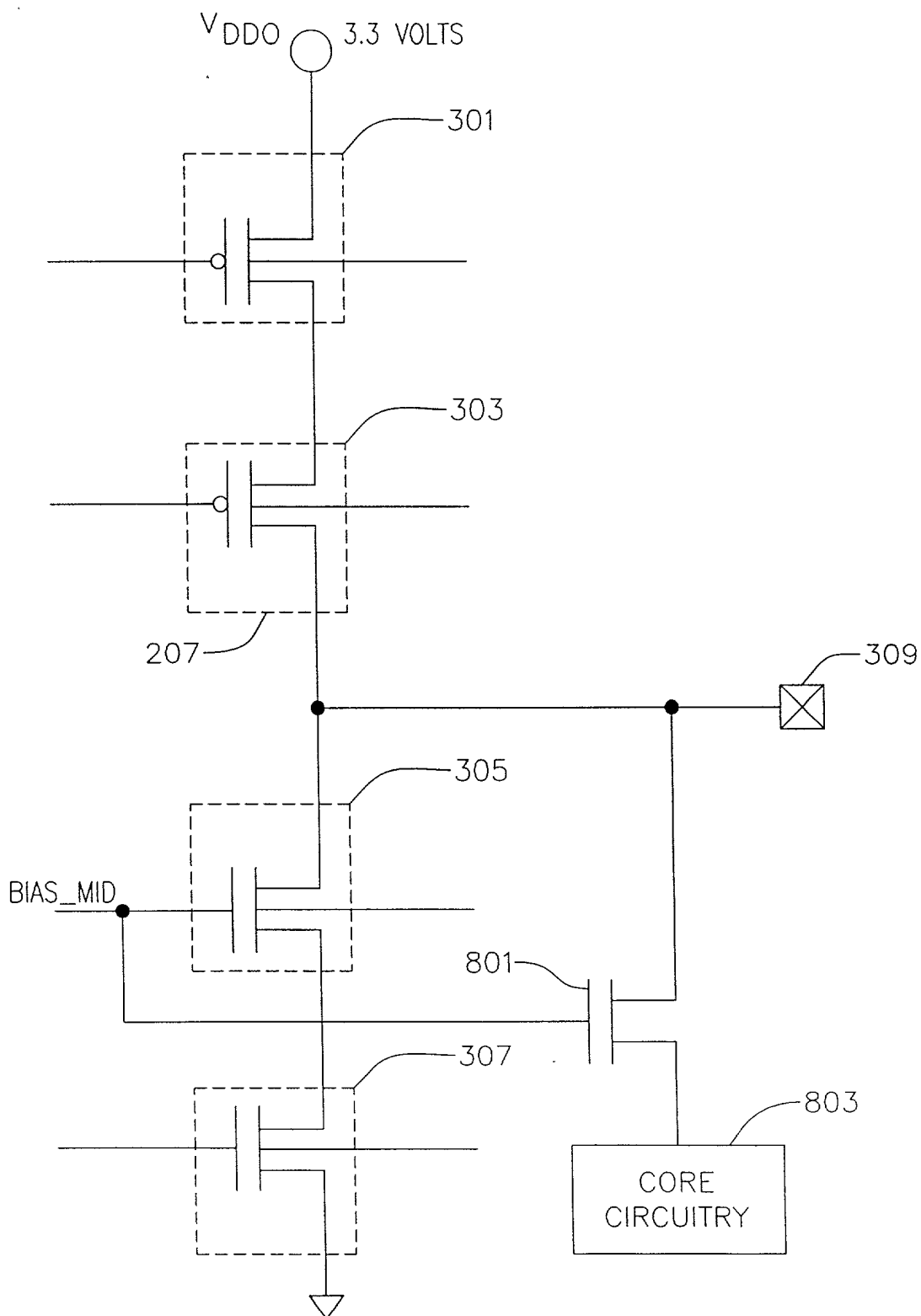




FIG. 9A

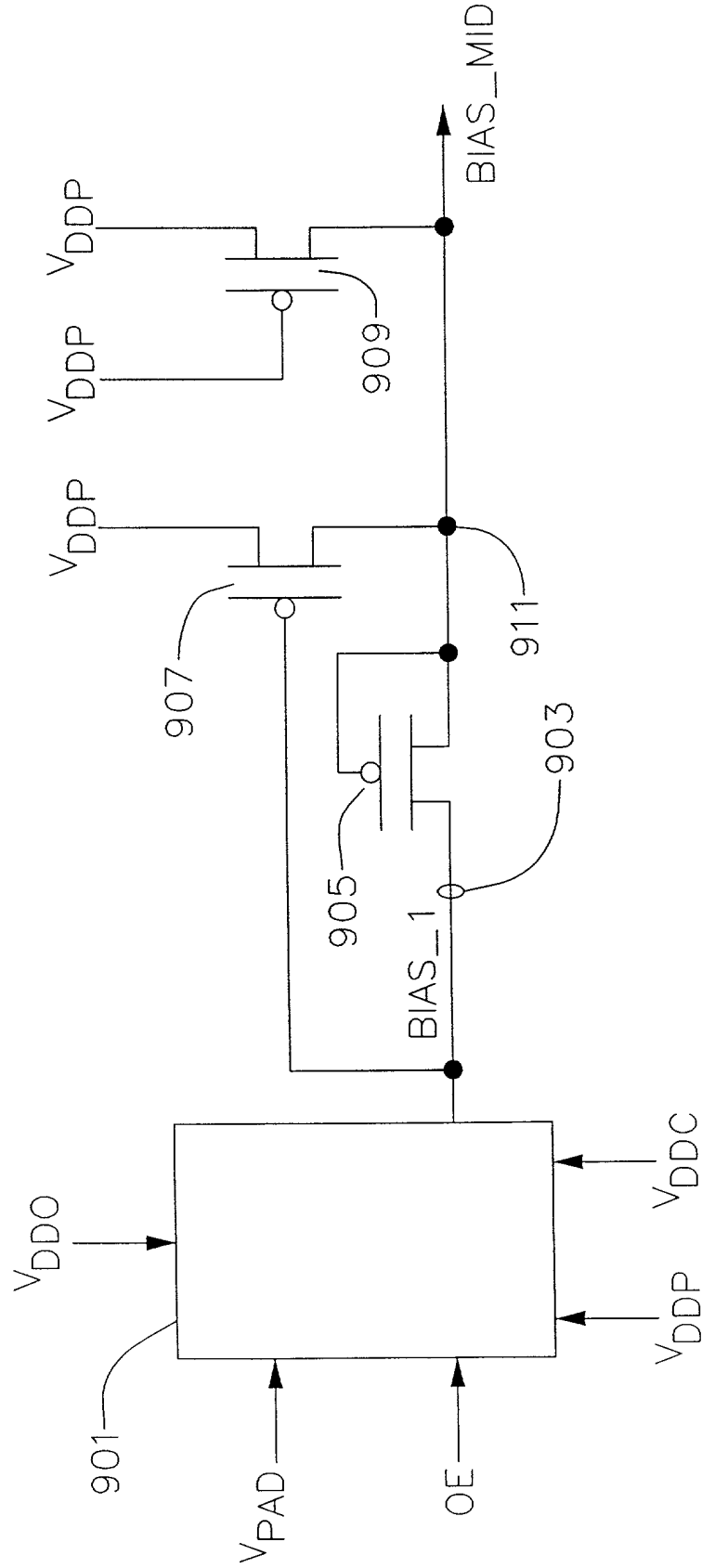




FIG. 10

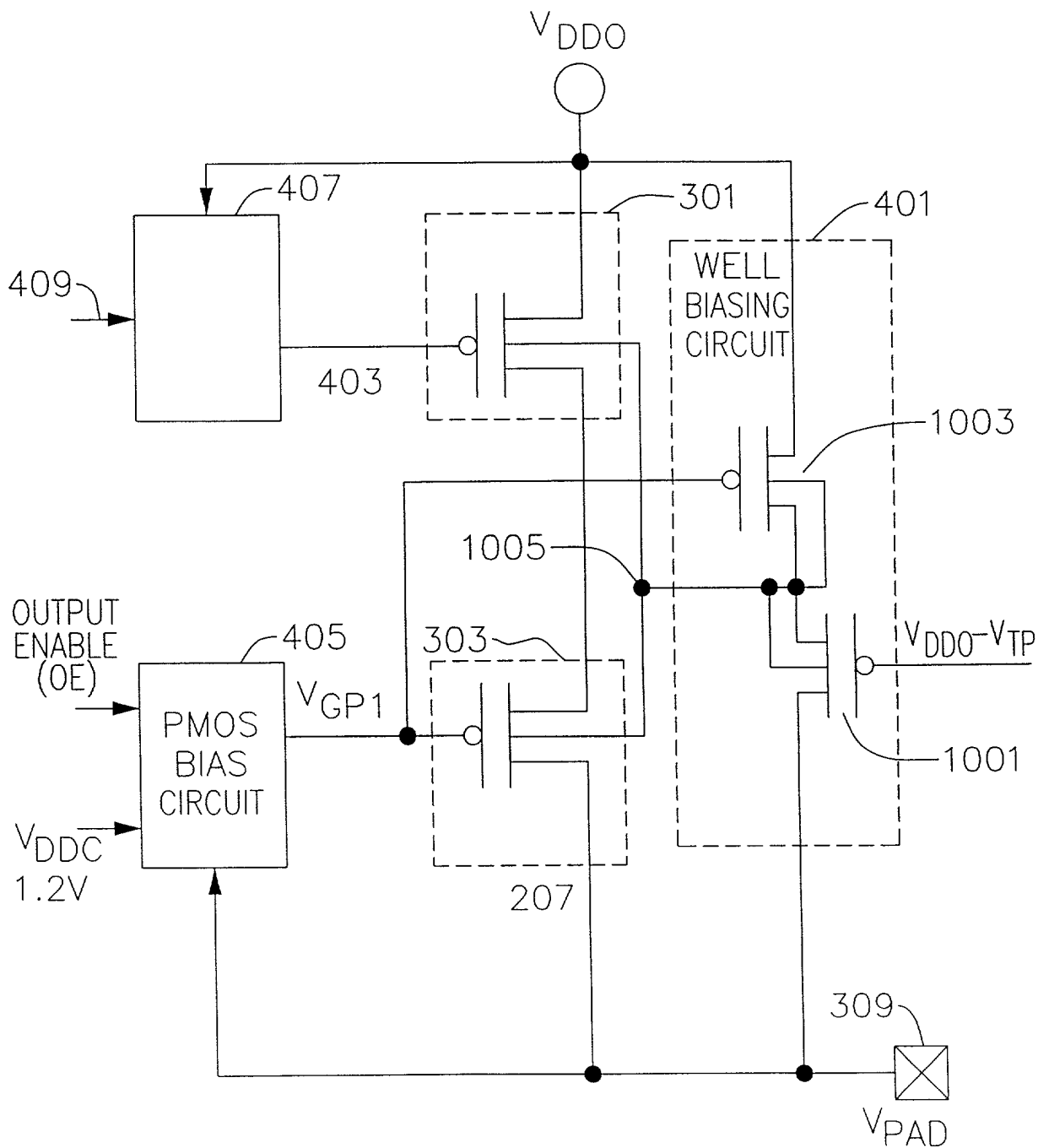
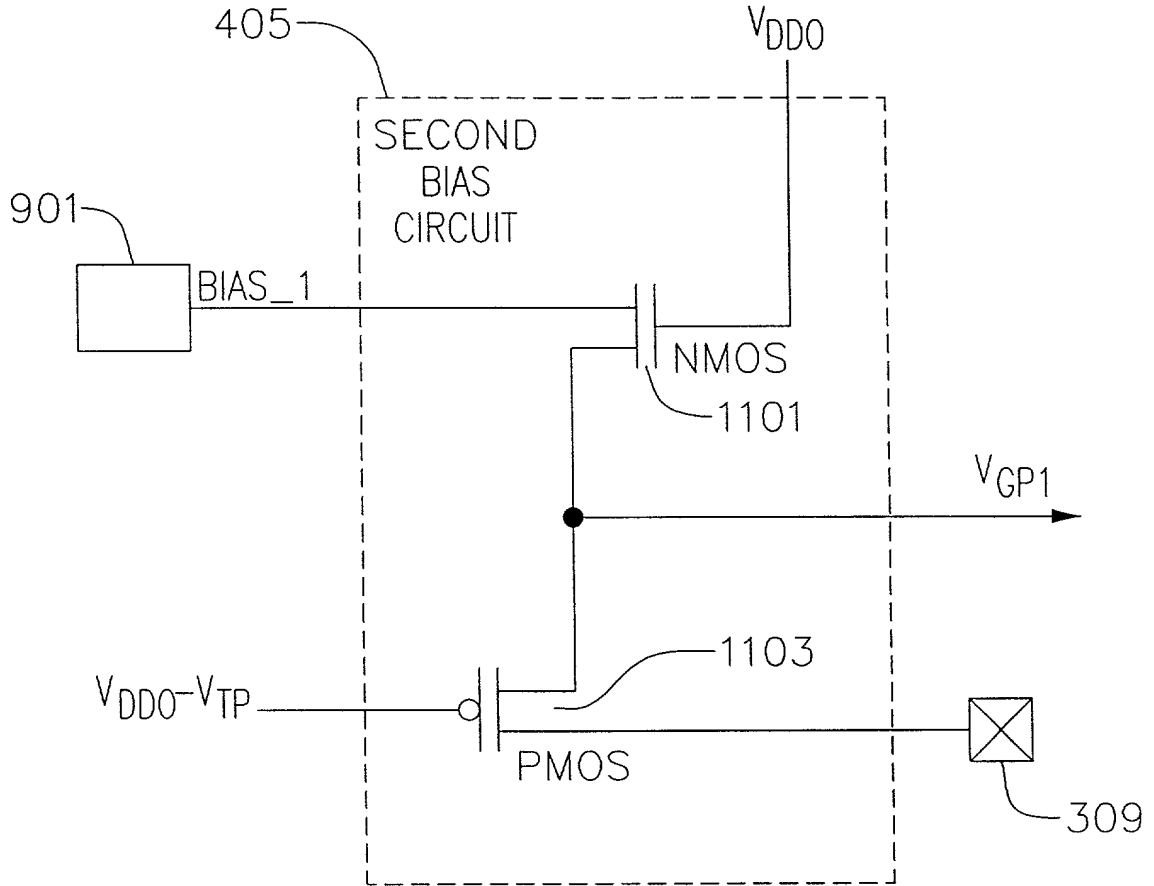




FIG. 11A



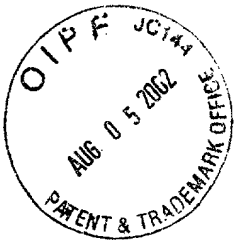
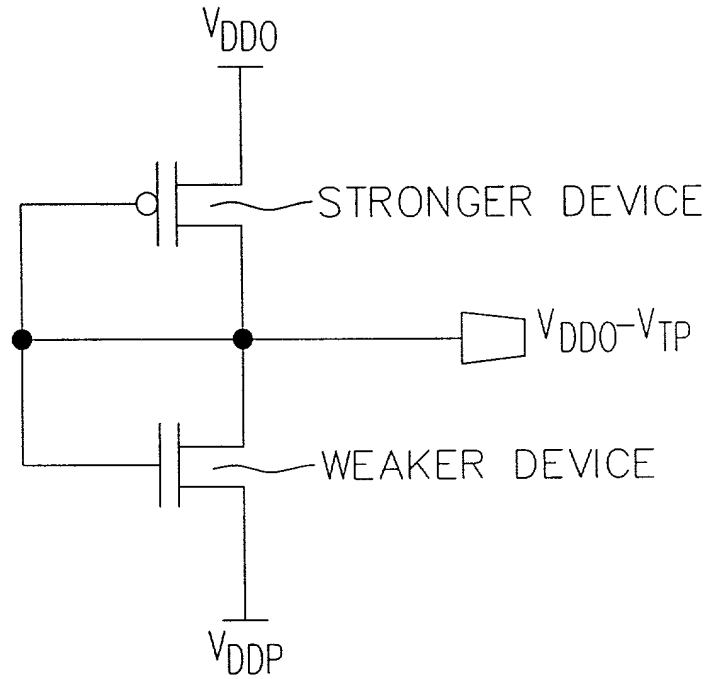


FIG. 11B

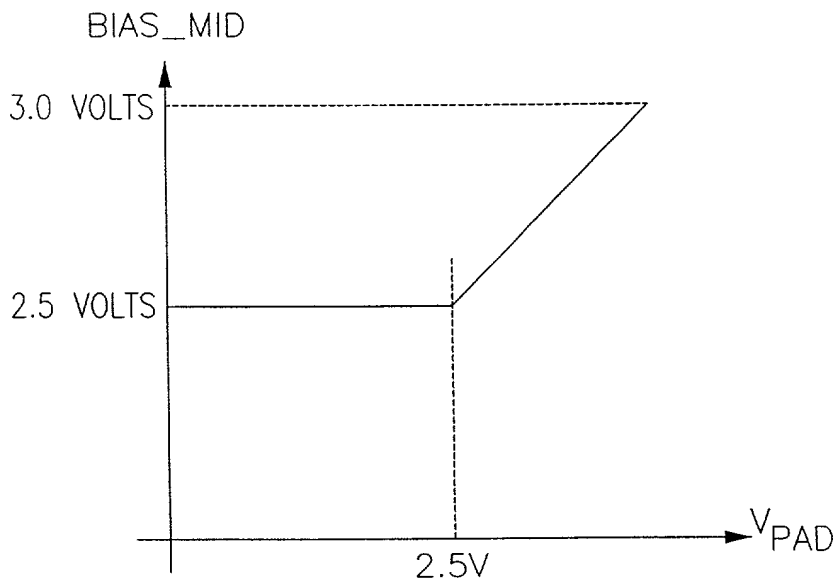


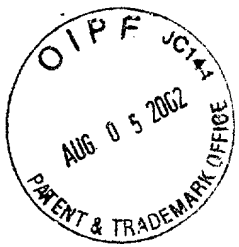


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FIG. 11C





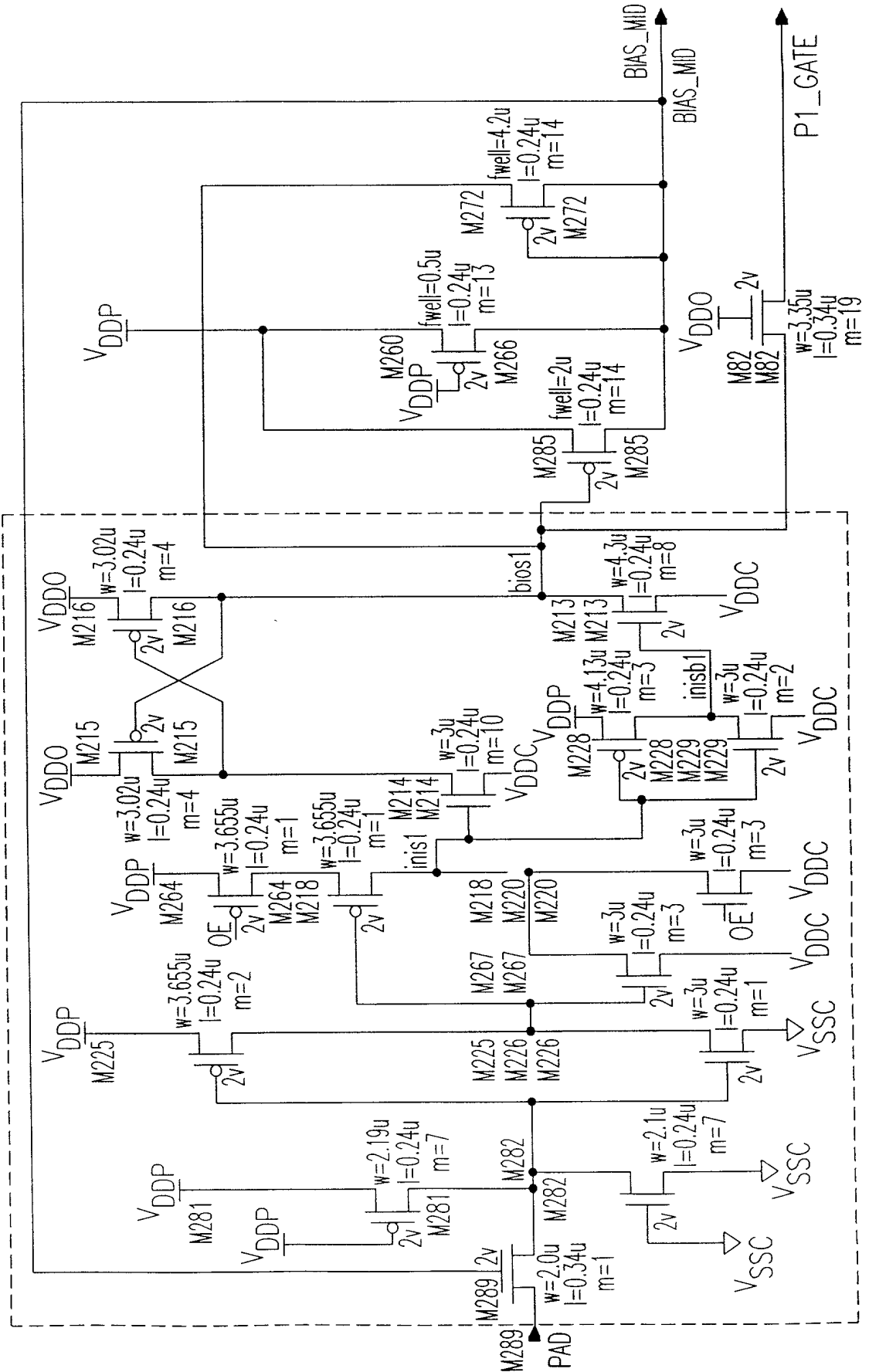
Application No. 10/043,763
Inventor(s): Janardhanan S Ajit
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VARIATIONS

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FIG. 11D

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TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH
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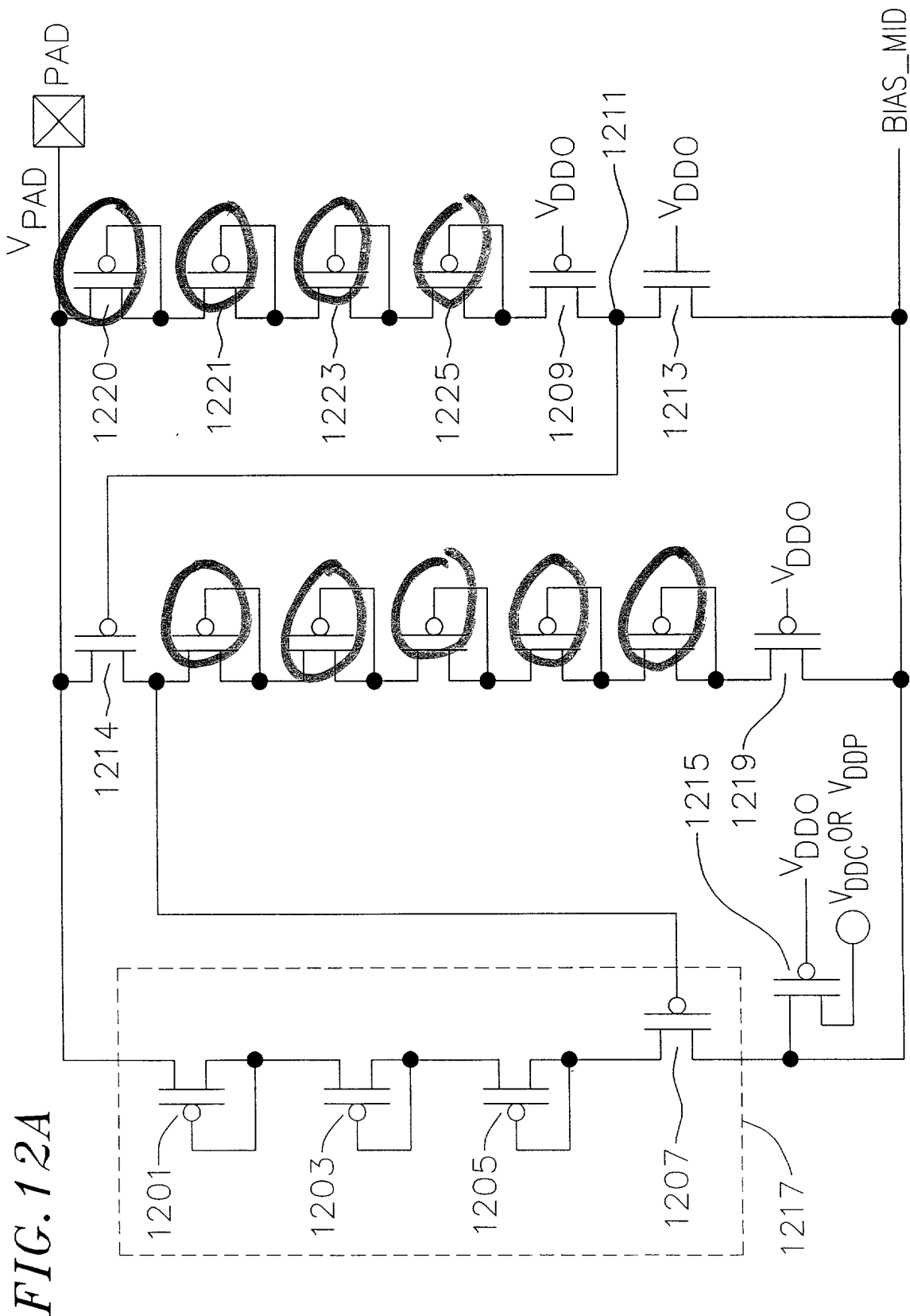
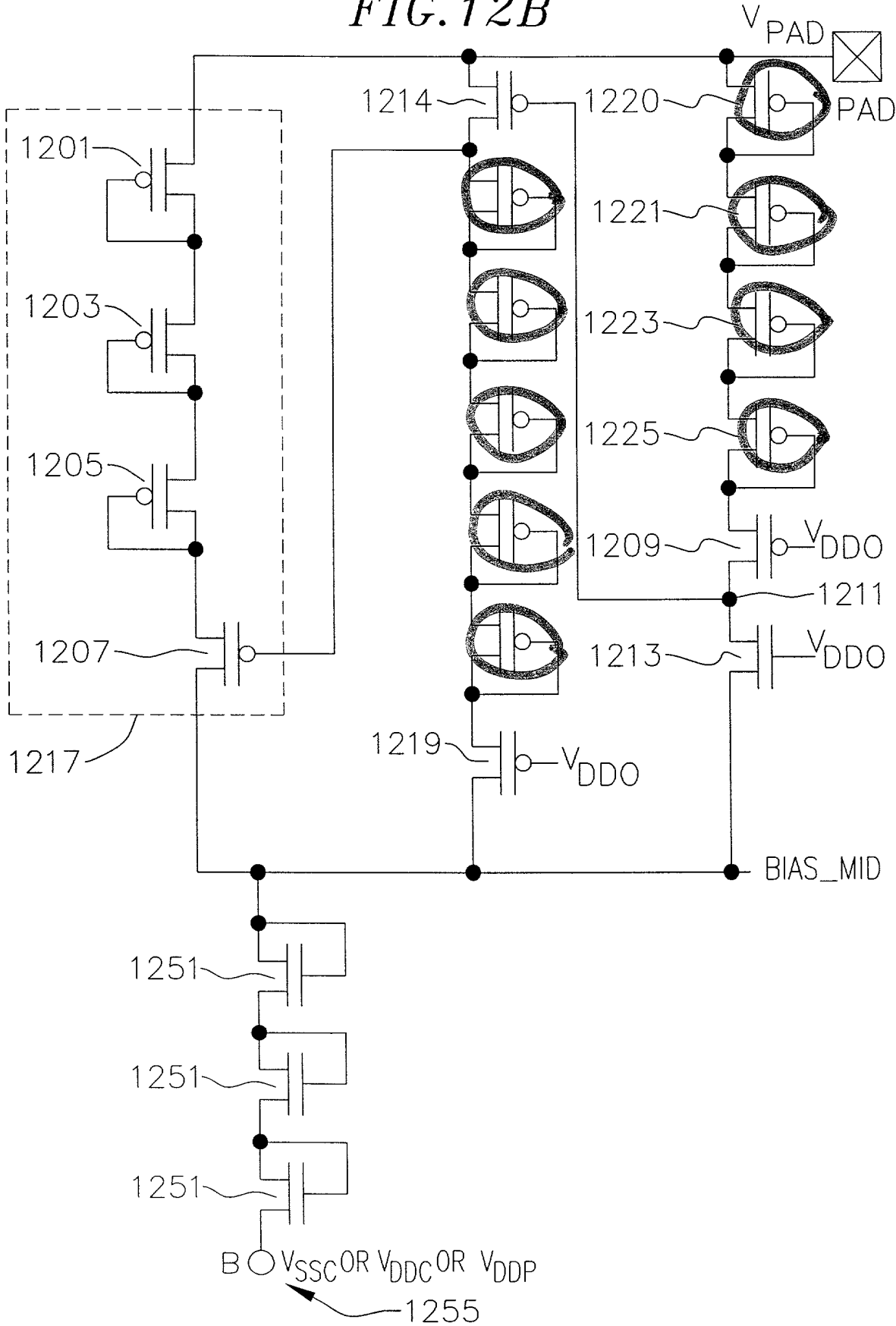




FIG. 12B

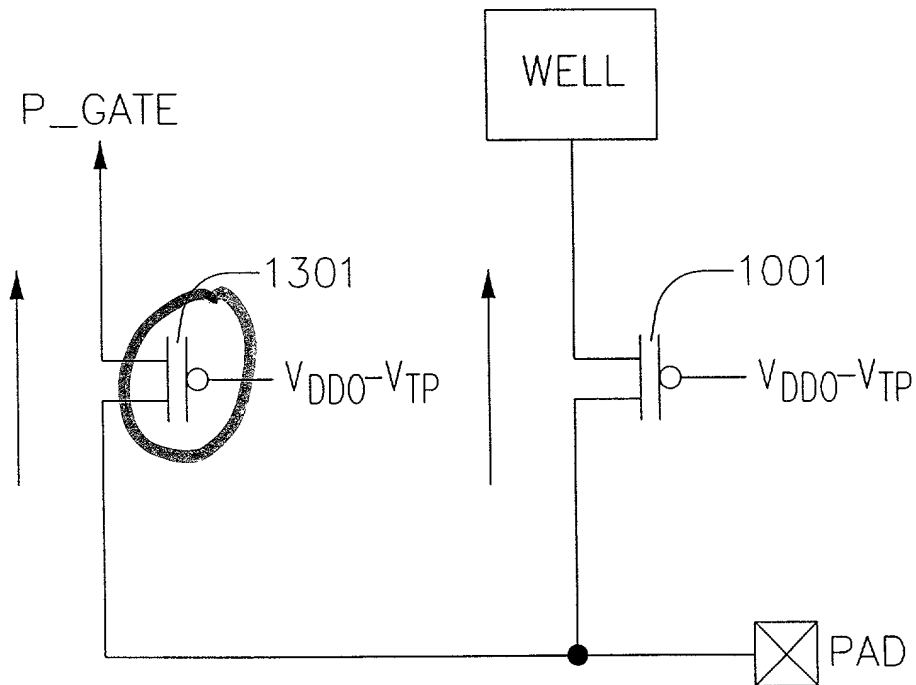




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FIG. 13

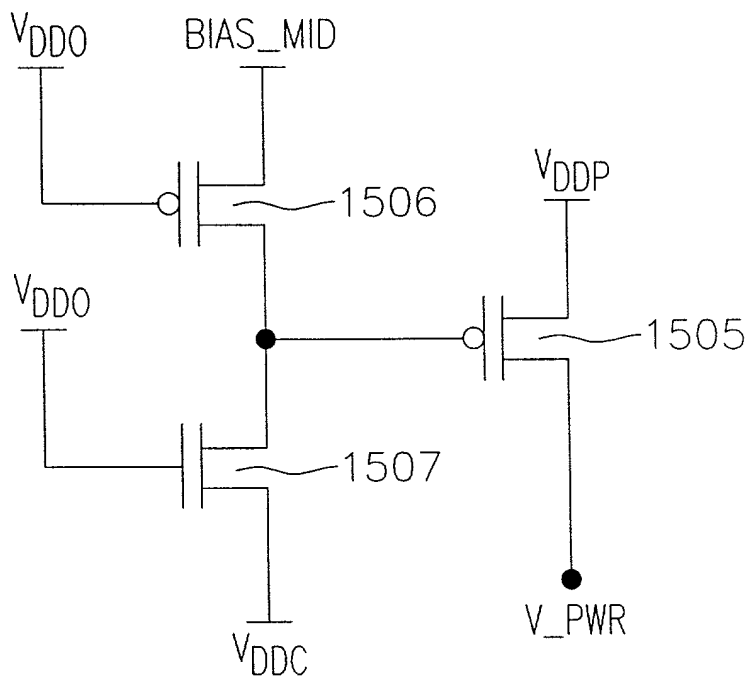




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FIG. 15



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FIG. 16

